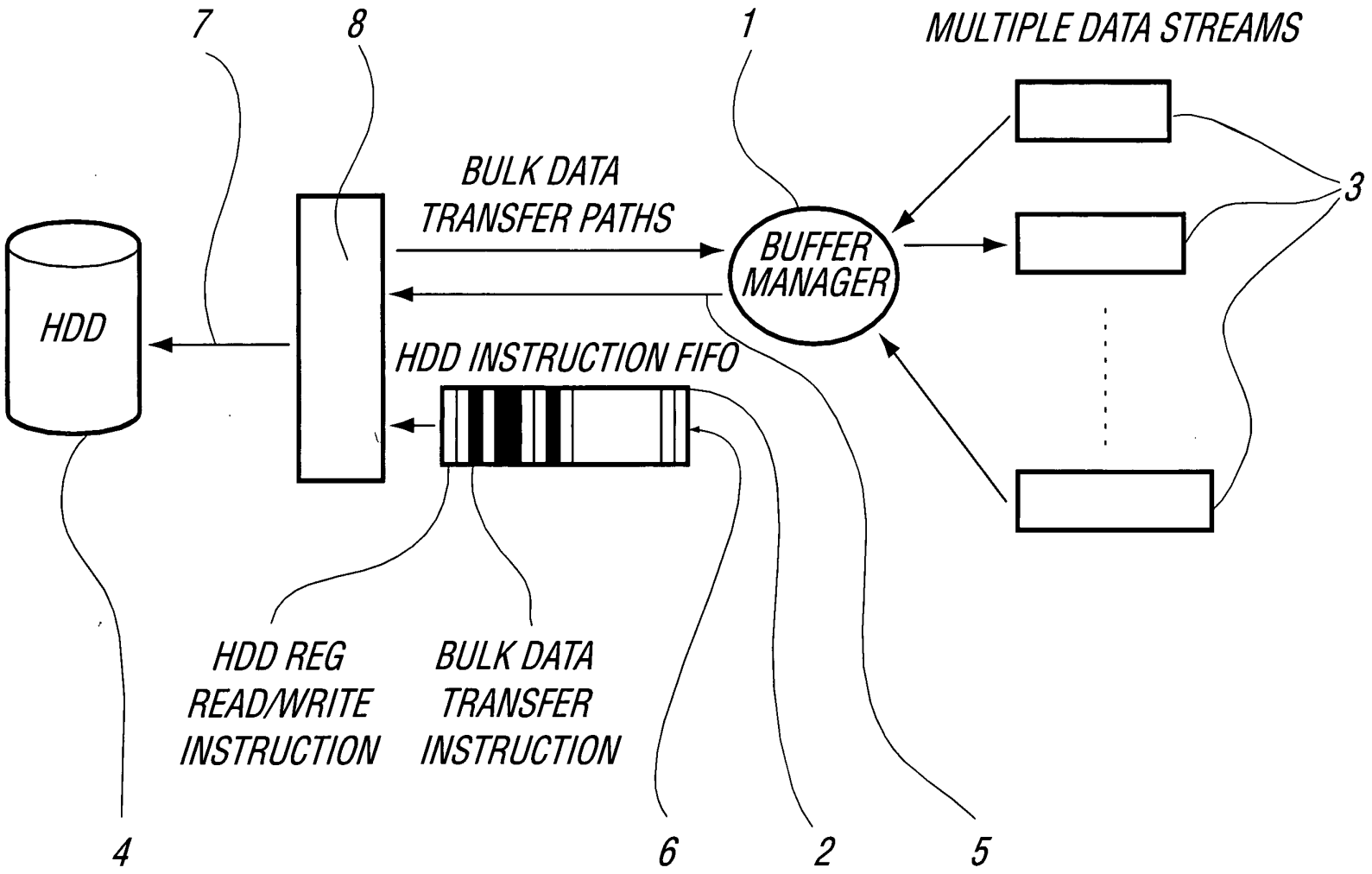

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1
MULTIPLE DATA STREAMS
BULK DATA
TRANSFER PATHS
BUFFER
MANAGER
3
HDD
HDD INSTRUCTION FIFO
HDD REG
READ/WRITE
INSTRUCTION
BULK DATA
TRANSFER
INSTRUCTION
4
6
2
5

FIG. 1

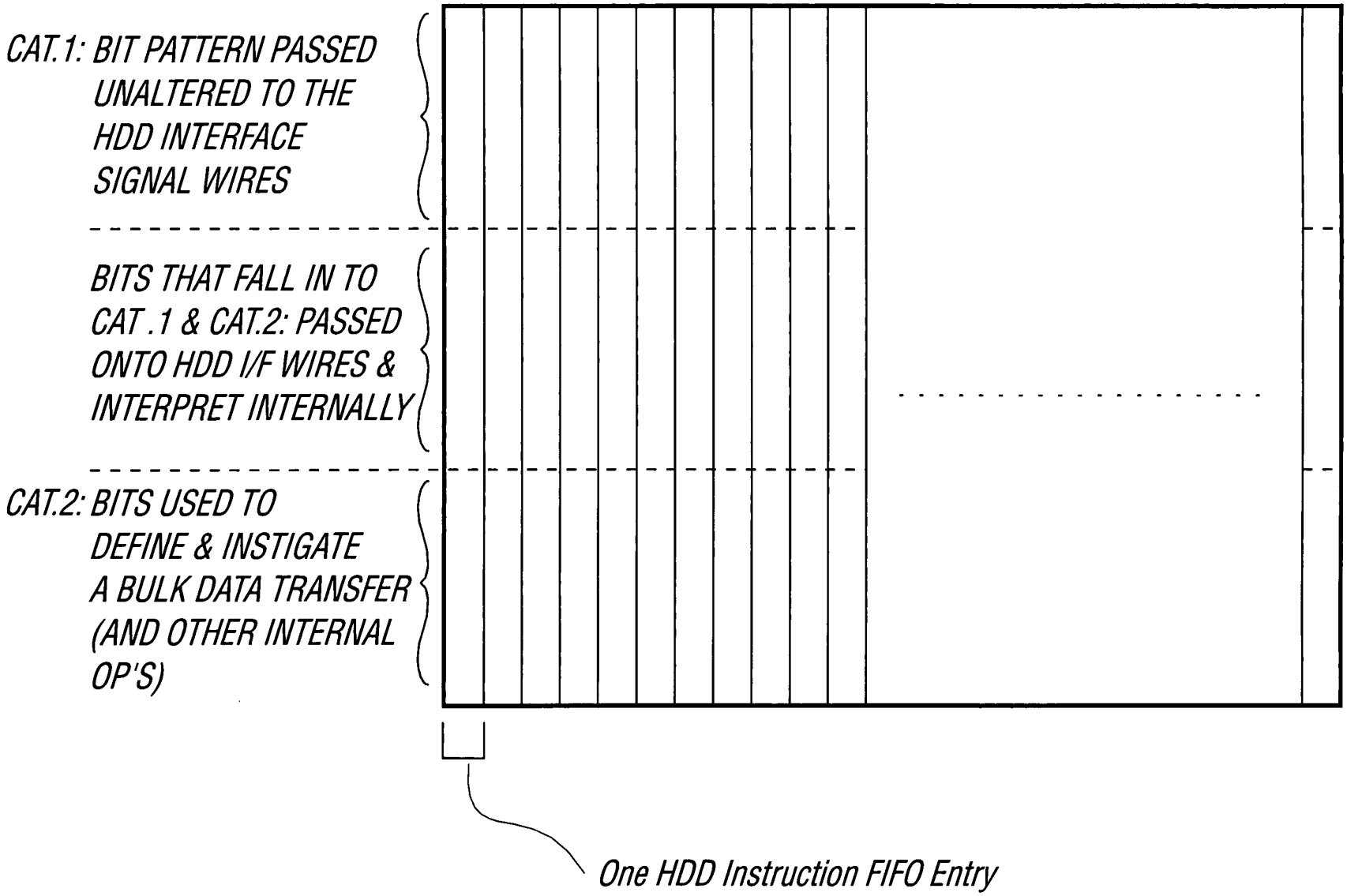

CAT.1: BIT PATTERN PASSED UNALTERED TO THE HDD INTERFACE SIGNAL WIRES
BITS THAT FALL IN TO CAT .1 & CAT.2: PASSED ONTO HDD I/F WIRES & INTERPRET INTERNALLY
CAT.2: BITS USED TO DEFINE & INSTIGATE A BULK DATA TRANSFER (AND OTHER INTERNAL OP'S)
One HDD Instruction FIFO Entry

FIG. 2

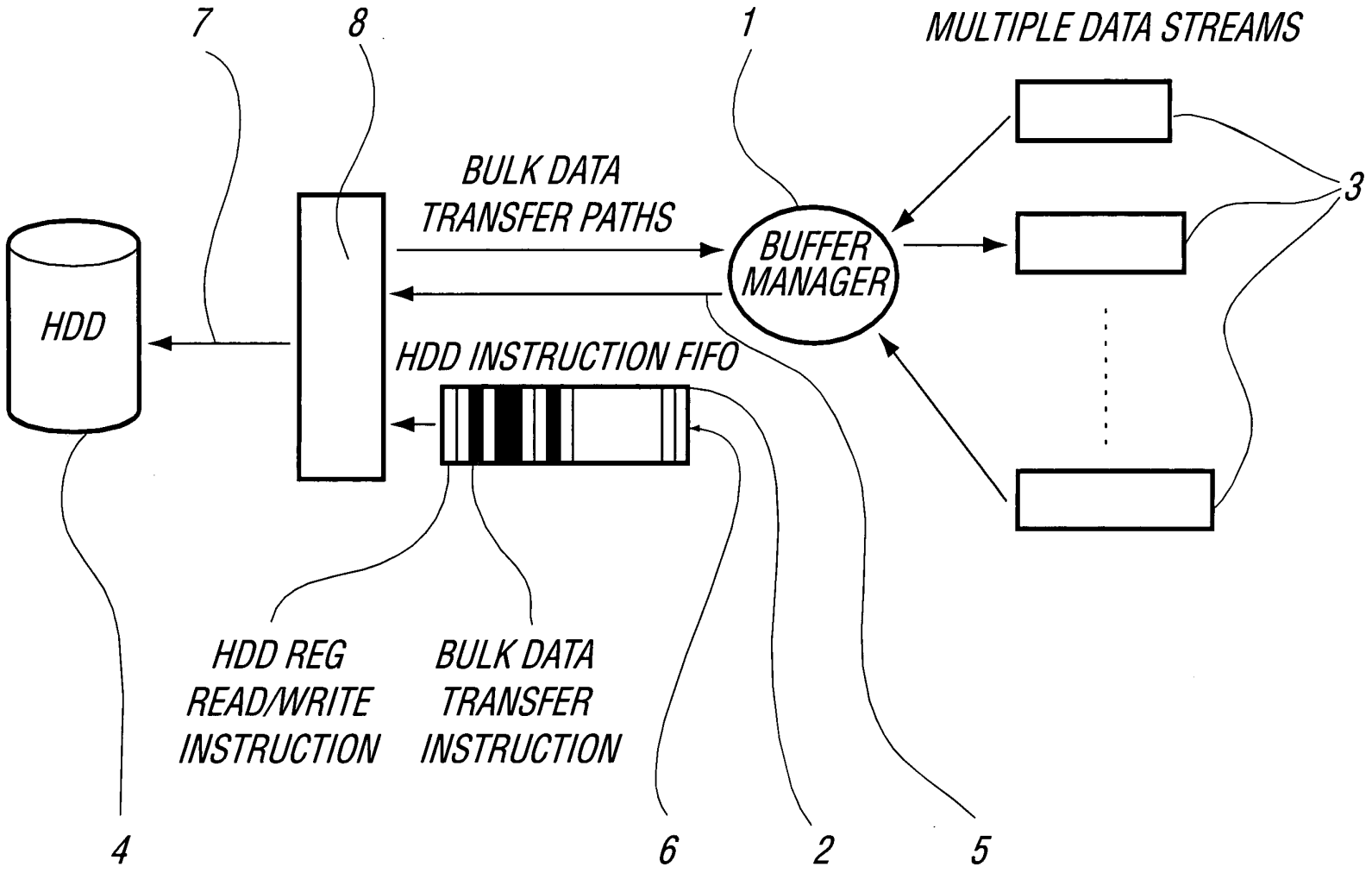

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FIG. 1

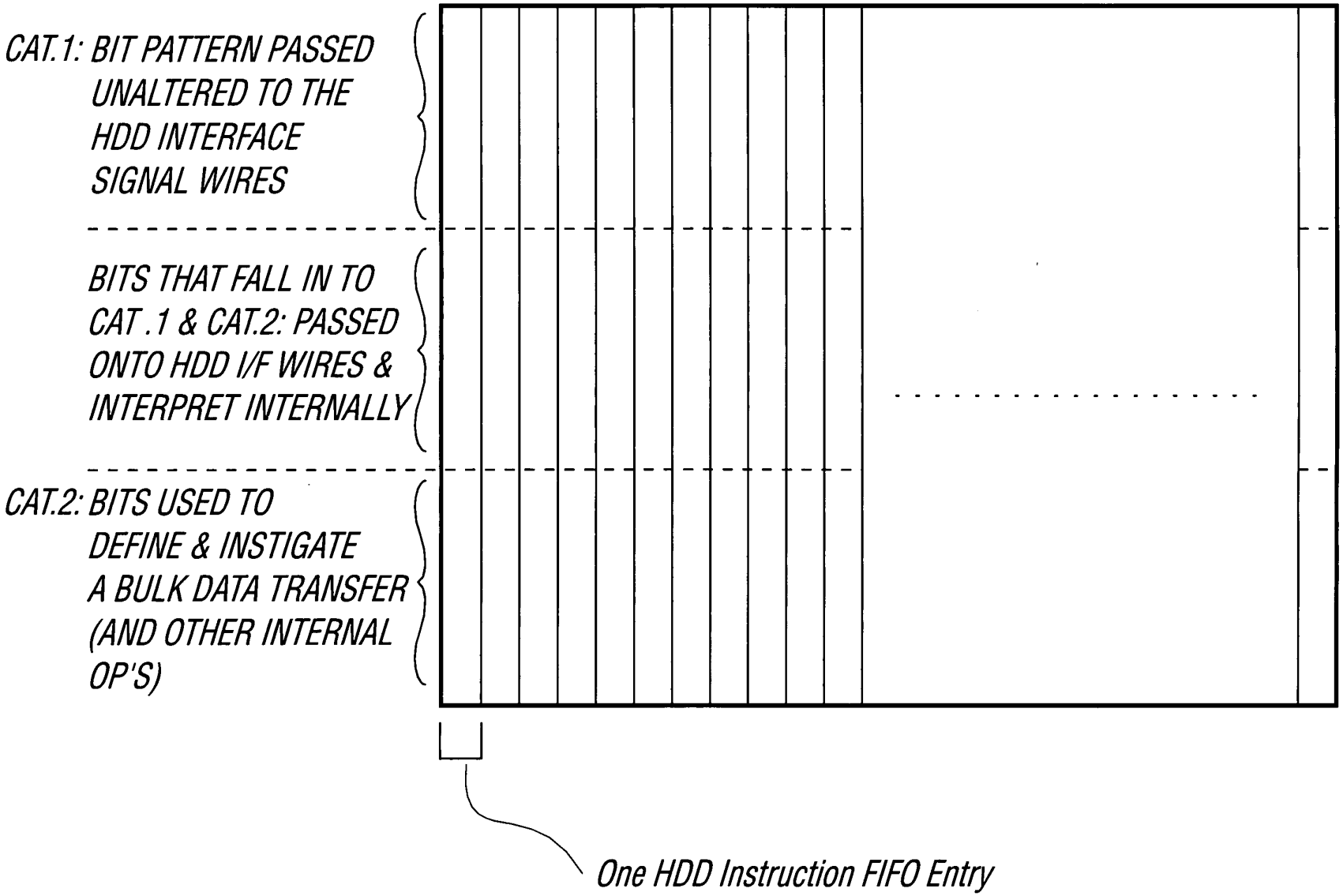

CAT.1: BIT PATTERN PASSED UNALTERED TO THE HDD INTERFACE SIGNAL WIRES
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FIG. 2